

MJE18006

Preferred Device

SWITCHMODE™

NPN Bipolar Power Transistor For Switching Power Supply Applications

The MJE18006 has an applications specific state-of-the-art die designed for use in 220 V line-operated SWITCHMODE Power supplies and electronic light ballasts.

Features

- Improved Efficiency Due to Low Base Drive Requirements:
 - ◆ High and Flat DC Current Gain h_{FE}
 - ◆ Fast Switching
 - ◆ No Coil Required in Base Circuit for Turn-Off (No Current Tail)
- Tight Parametric Distributions are Consistent Lot-to-Lot
- Standard TO-220
- Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Sustaining Voltage	V_{CEO}	450	Vdc
Collector-Emitter Breakdown Voltage	V_{CES}	1000	Vdc
Emitter-Base Voltage	V_{EBO}	9.0	Vdc
Collector Current – Continuous	I_C	6.0	Adc
– Peak (Note 1)	I_{CM}	15	
Base Current – Continuous	I_B	4.0	Adc
– Peak (Note 1)	I_{BM}	8.0	
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	100 0.8	W W/ $^\circ\text{C}$
Operating and Storage Temperature	T_J, T_{stg}	-65 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.25	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds	T_L	260	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Pulse Test: Pulse Width = 5 ms, Duty Cycle $\leq 10\%$.

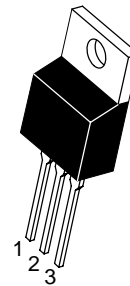
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



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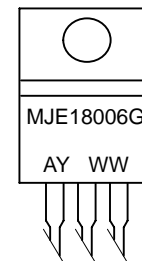
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**POWER TRANSISTOR
6.0 AMPERES
1000 VOLTS – 100 WATTS**



TO-220AB
CASE 221A-09
STYLE 1

MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MJE18006	TO-220	50 Units / Rail
MJE18006G	TO-220 (Pb-Free)	50 Units / Rail

Preferred devices are recommended choices for future use and best overall value.

MJE18006

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit			
OFF CHARACTERISTICS								
Collector–Emitter Sustaining Voltage (I _C = 100 mA, L = 25 mH)	V _{CEO(sus)}	450	–	–	Vdc			
Collector Cutoff Current (V _{CE} = Rated V _{CEO} , I _B = 0)	I _{CEO}	–	–	100	μAdc			
Collector Cutoff Current (V _{CE} = Rated V _{CEs} , V _{EB} = 0)	I _{CES}	–	–	100	μAdc			
(V _{CE} = 800 V, V _{EB} = 0)	(T _C = 125°C)	–	–	500				
	(T _C = 125°C)	–	–	100				
Emitter Cutoff Current (V _{EB} = 9.0 Vdc, I _C = 0)	I _{EBO}	–	–	100	μAdc			
ON CHARACTERISTICS								
Base–Emitter Saturation Voltage (I _C = 1.3 Adc, I _B = 0.13 Adc)	V _{BE(sat)}	–	0.83	1.2	Vdc			
(I _C = 3.0 Adc, I _B = 0.6 Adc)		–	0.94	1.3				
Collector–Emitter Saturation Voltage (I _C = 1.3 Adc, I _B = 0.13 Adc)	V _{CE(sat)}	–	0.25	0.6	Vdc			
(I _C = 3.0 Adc, I _B = 0.6 Adc)	(T _C = 125°C)	–	0.27	0.65				
	(T _C = 125°C)	–	0.35	0.7				
	(T _C = 125°C)	–	0.4	0.8				
DC Current Gain (I _C = 0.5 Adc, V _{CE} = 5.0 Vdc)	h _{FE}	14	–	34	–			
(I _C = 3.0 Adc, V _{CE} = 1.0 Vdc)	(T _C = 125°C)	–	32	–				
	(T _C = 125°C)	6.0	10	–				
	(T _C = 125°C)	5.0	8.0	–				
(I _C = 1.3 Adc, V _{CE} = 1.0 Vdc)	(T _C = 25 to 125°C)	11	17	–				
(I _C = 10 mAdc, V _{CE} = 5.0 Vdc)		10	22	–				
DYNAMIC CHARACTERISTICS								
Current Gain Bandwidth (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 MHz)	f _T	–	14	–	MHz			
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	–	75	120	pF			
Input Capacitance (V _{EB} = 8.0 V)	C _{ib}	–	1000	1500	pF			
Dynamic Saturation Voltage:	V _{CE(dsat)}	(I _C = 1.3 Adc, I _{B1} = 130 mAdc, V _{CC} = 300 V)	1.0 μs	(T _C = 125°C)	–	5.5	–	V
Determined 1.0 μs and 3.0 μs respectively after rising I _{B1} reaches 90% of final I _{B1} (see Figure 18)			3.0 μs	(T _C = 125°C)	–	3.0	–	
		(I _C = 3.0 Adc, I _{B1} = 0.6 Adc, V _{CC} = 300 V)	1.0 μs	(T _C = 125°C)	–	9.5	–	
			3.0 μs	(T _C = 125°C)	–	14.5	–	
						–	2.0	–
					–	7.5	–	
SWITCHING CHARACTERISTICS: Resistive Load (D.C. ≤ 10%, Pulse Width = 20 μs)								
Turn–On Time	(I _C = 3.0 Adc, I _{B1} = 0.6 Adc, I _{B2} = 1.5 Adc, V _{CC} = 300 V)	(T _C = 125°C)	t _{on}	–	90	180	ns	
Turn–Off Time			t _{off}	–	1.7	2.5	μs	
		(T _C = 125°C)		–	2.1	–		
Turn–On Time	(I _C = 1.3 Adc, I _{B1} = 0.13 Adc, I _{B2} = 0.65 Adc, V _{CC} = 300 V)	(T _C = 125°C)	t _{on}	–	200	300	ns	
Turn–Off Time			t _{off}	–	1.2	2.5	μs	
		(T _C = 125°C)		–	1.5	–		
SWITCHING CHARACTERISTICS: Inductive Load (V_{clamp} = 300 V, V_{CC} = 15 V, L = 200 μH)								
Fall Time	(I _C = 1.5 Adc, I _{B1} = 0.13 Adc, I _{B2} = 0.65 Adc)	(T _C = 125°C)	t _{fi}	–	100	180	ns	
Storage Time			t _{si}	–	1.5	2.5	μs	
Crossover Time			t _c	–	1.9	–		
		(T _C = 125°C)		–	220	350	ns	
		(T _C = 125°C)		–	230	–		
Fall Time	(I _C = 3.0 Adc, I _{B1} = 0.6 Adc, I _{B2} = 1.5 Adc)	(T _C = 125°C)	t _{fi}	–	85	150	ns	
Storage Time			t _{si}	–	2.15	3.2	μs	
Crossover Time			t _c	–	2.75	–		
		(T _C = 125°C)		–	200	300	ns	
		(T _C = 125°C)		–	310	–		

2. Proper strike and creepage distance must be provided.

TYPICAL STATIC CHARACTERISTICS

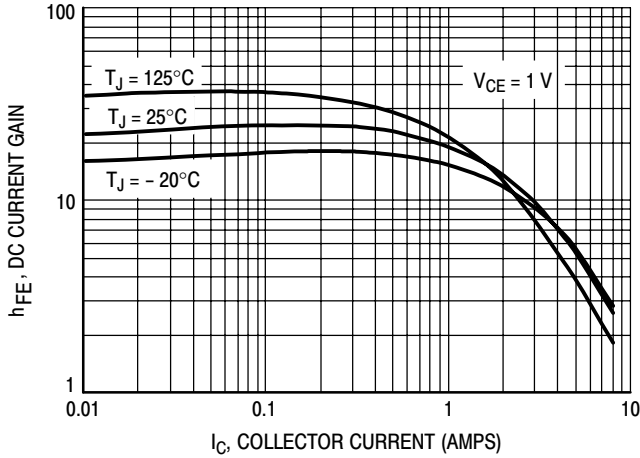


Figure 1. DC Current Gain @ 1 Volt

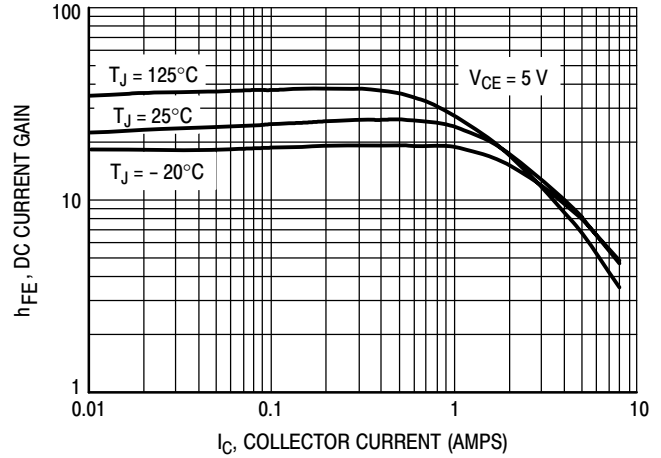


Figure 2. DC Current Gain @ 5 Volts

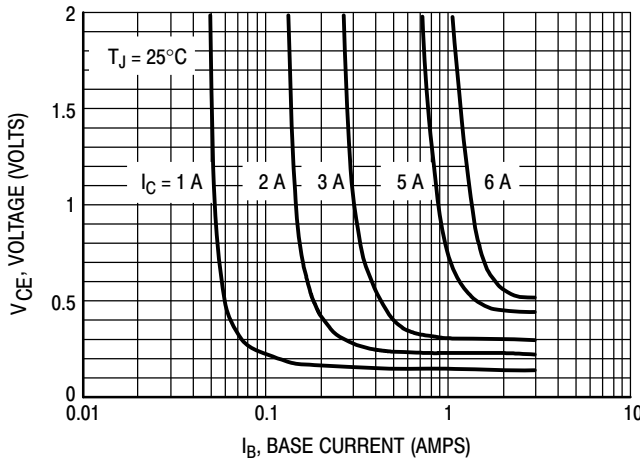


Figure 3. Collector Saturation Region

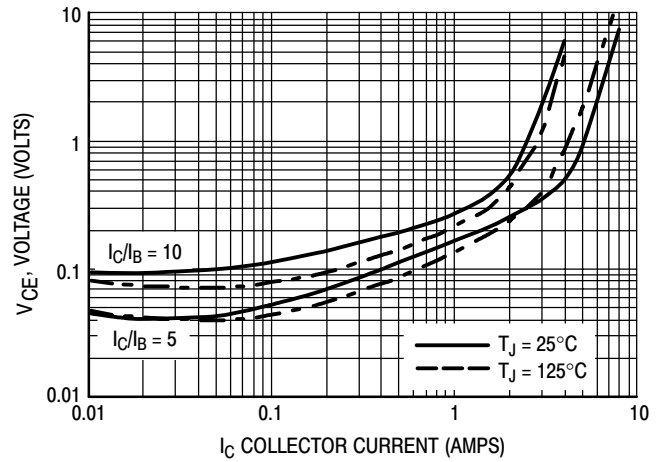


Figure 4. Collector-Emitter Saturation Voltage

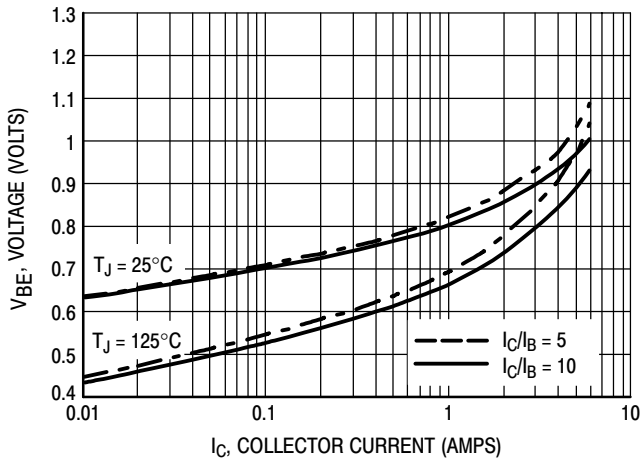


Figure 5. Base-Emitter Saturation Region

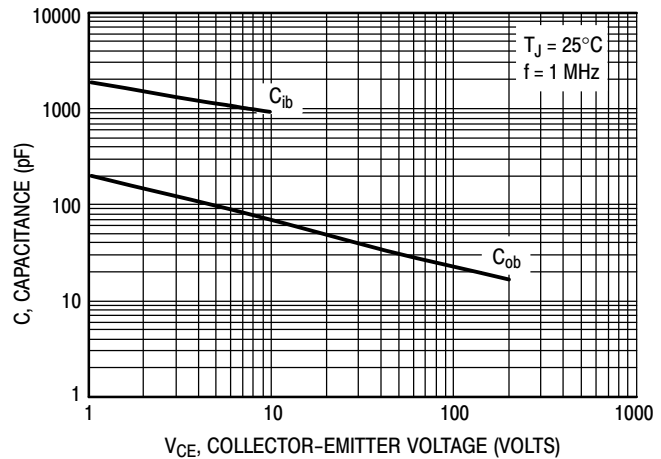


Figure 6. Capacitance

TYPICAL SWITCHING CHARACTERISTICS
($I_{B2} = I_C/2$ for all switching)

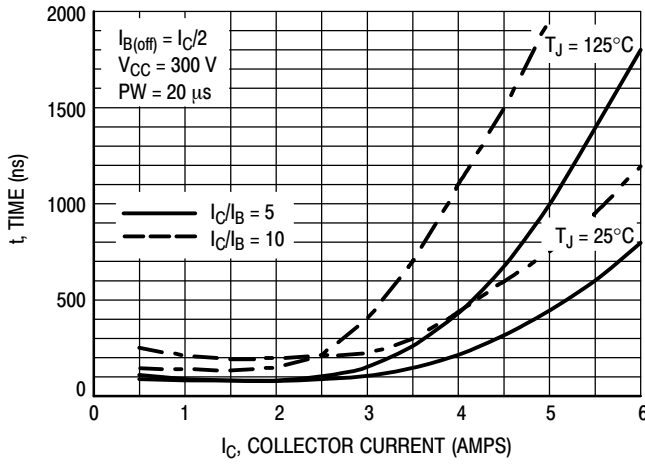


Figure 7. Resistive Switching, t_{on}

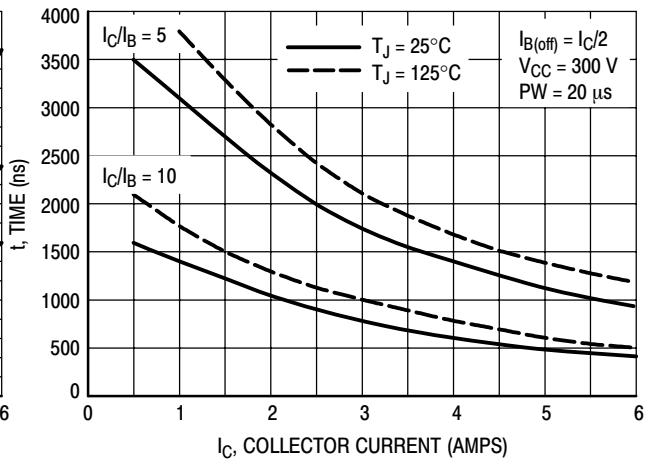


Figure 8. Resistive Switching, t_{off}

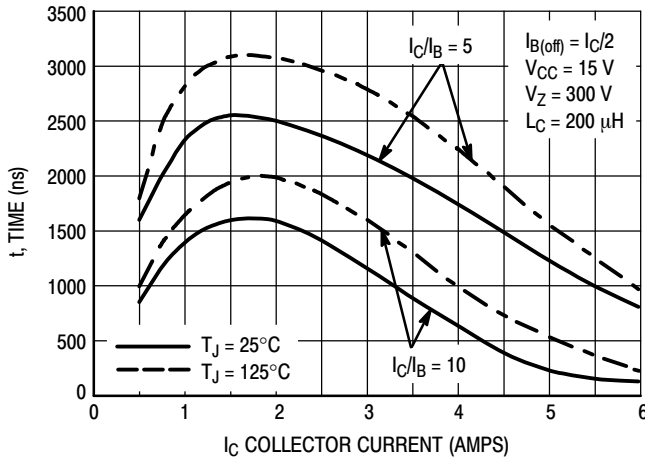


Figure 9. Inductive Storage Time, t_{si}

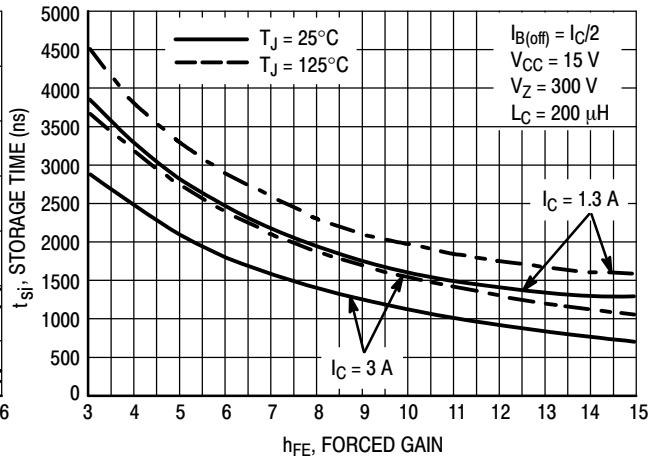


Figure 10. Inductive Storage Time, $t_{si}(h_{FE})$

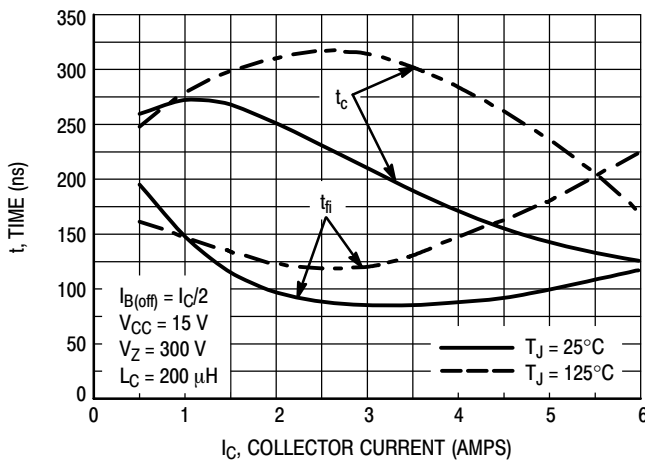


Figure 11. Inductive Switching, t_c and t_{fi}
 $I_C/I_B = 5$

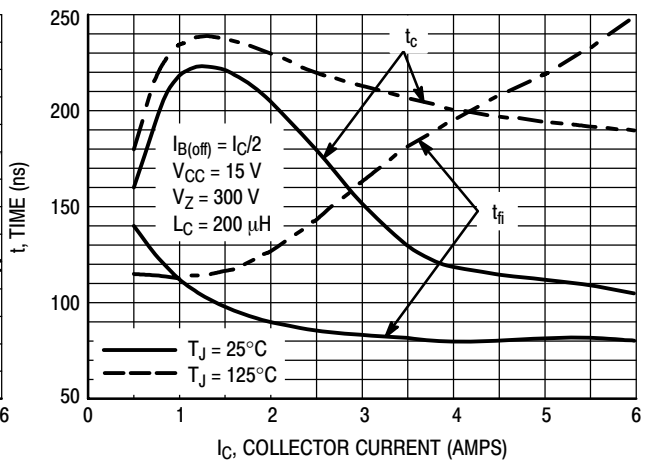


Figure 12. Inductive Switching, t_c and t_{fi}
 $I_C/I_B = 10$

TYPICAL SWITCHING CHARACTERISTICS
($I_{B2} = I_C/2$ for all switching)

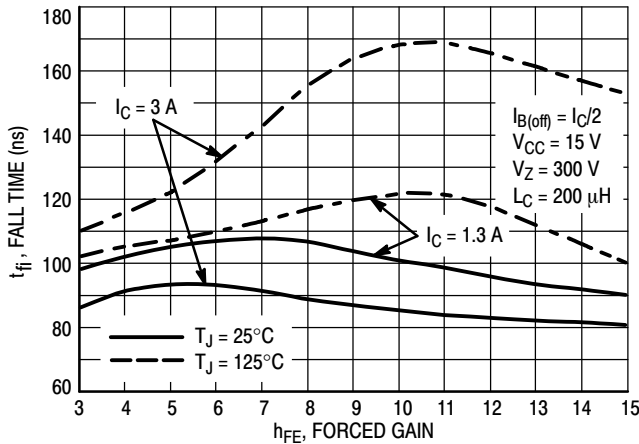


Figure 13. Inductive Fall Time

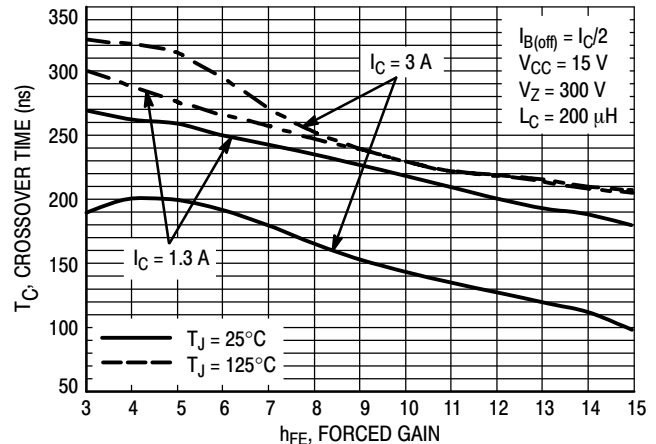


Figure 14. Inductive Crossover Time

GUARANTEED SAFE OPERATING AREA INFORMATION

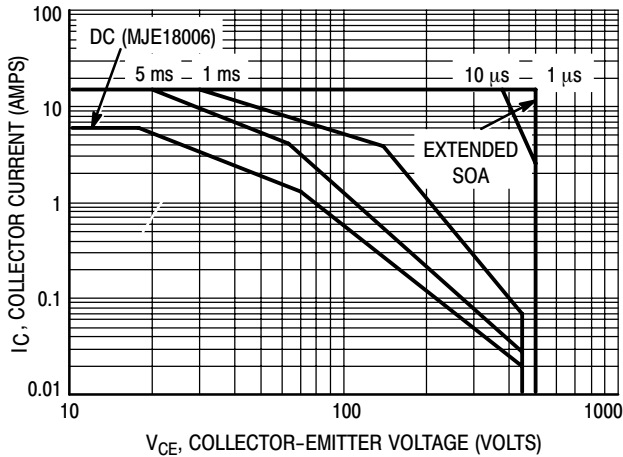


Figure 15. Forward Bias Safe Operating Area

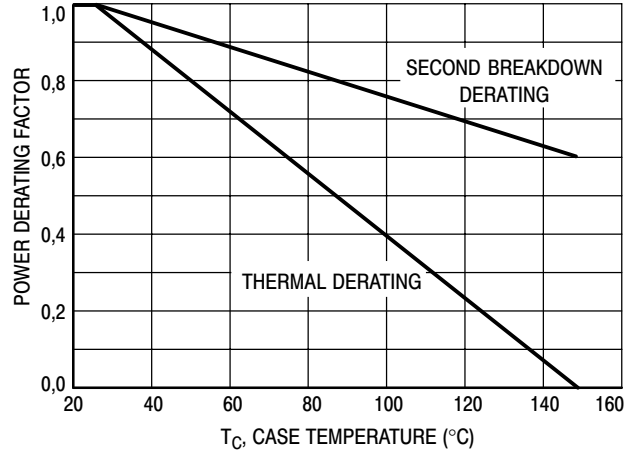


Figure 17. Forward Bias Power Derating

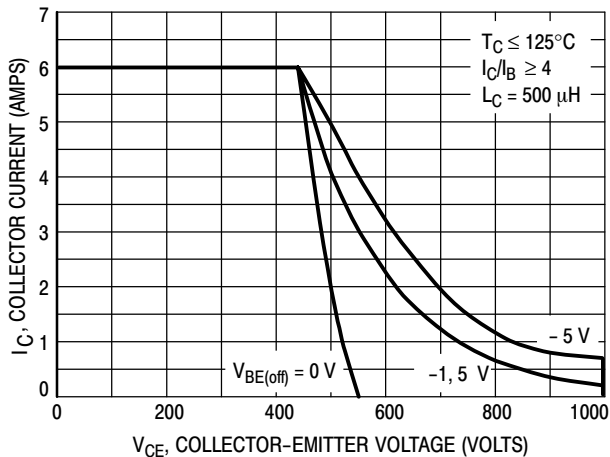


Figure 16. Reverse Bias Switching Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable

operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 15 is based on $T_C = 25^\circ\text{C}$; $T_{J(pk)}$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when $T_C \geq 25^\circ\text{C}$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown in Figure 15 may be found at any case temperature by using the appropriate curve on Figure 17. $T_{J(pk)}$ may be calculated from the data in Figure 20. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base-to-emitter junction reverse-biased. The safe level is specified as a reverse-biased safe operating area (Figure 16). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

MJE18006

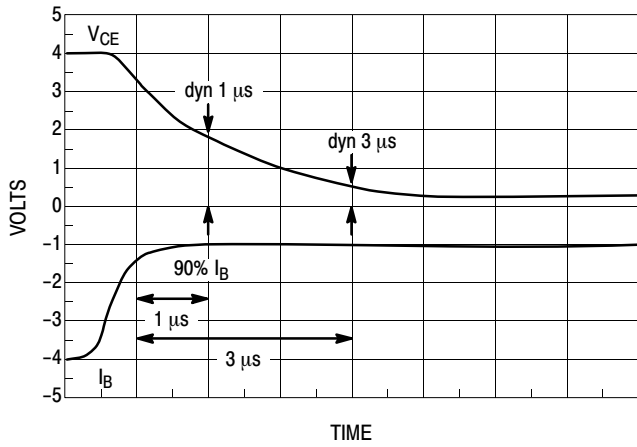


Figure 18. Dynamic Saturation Voltage Measurements

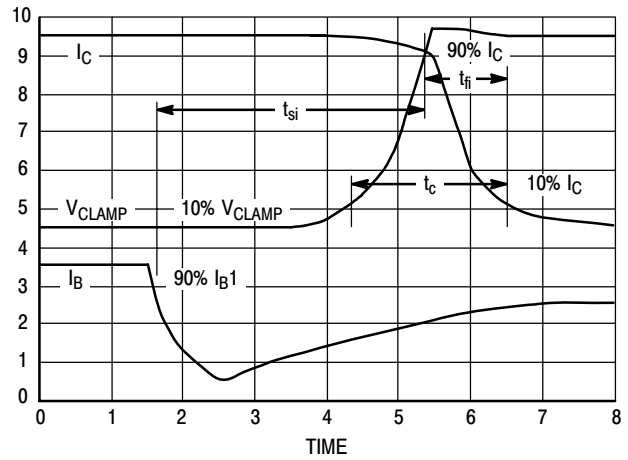
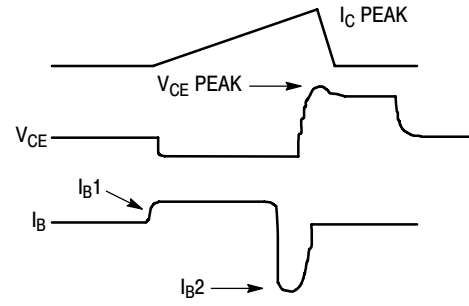
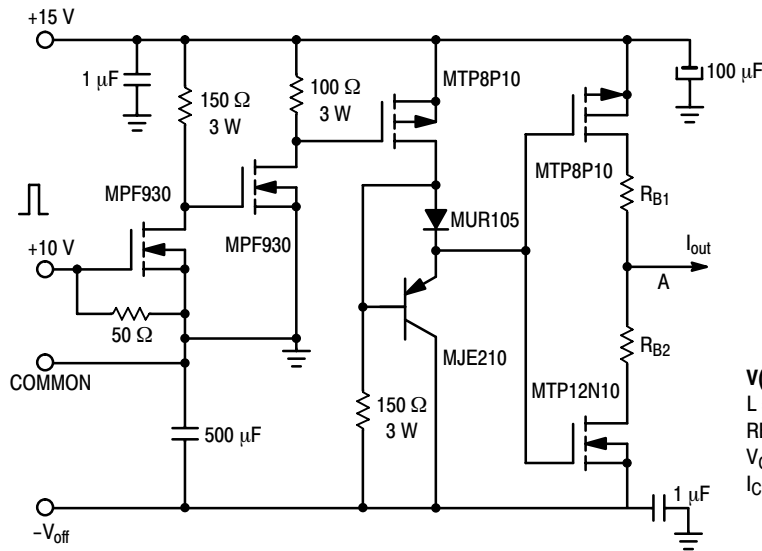


Figure 19. Inductive Switching Measurements



V(BR)CEO(sus)	INDUCTIVE SWITCHING	RBSOA
L = 10 mH	L = 200 μH	L = 500 μH
RB2 = ∞	RB2 = 0	RB2 = 0
VCC = 20 VOLTS	VCC = 15 VOLTS	VCC = 15 VOLTS
IC(pk) = 100 mA	RB1 SELECTED FOR DESIRED IB1	RB1 SELECTED FOR DESIRED IB1

Table 1. Inductive Load Switching Drive Circuit

TYPICAL THERMAL RESPONSE

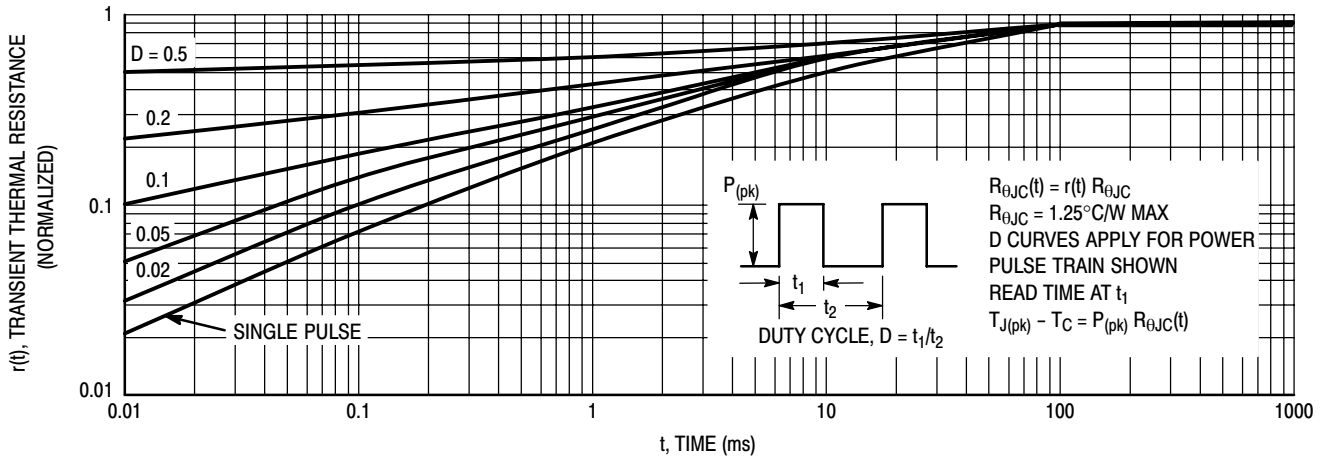
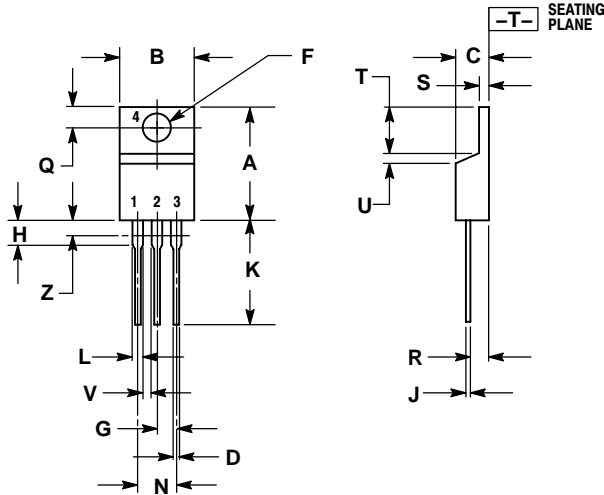


Figure 20. Typical Thermal Response ($Z_{\theta JC}(t)$) for MJE18006

MJE18006

PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 ISSUE AA



NOTES:


1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

STYLE 1:

1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

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